

FIG. 1

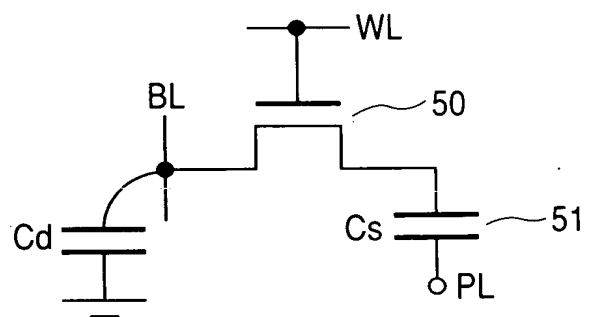


FIG. 2

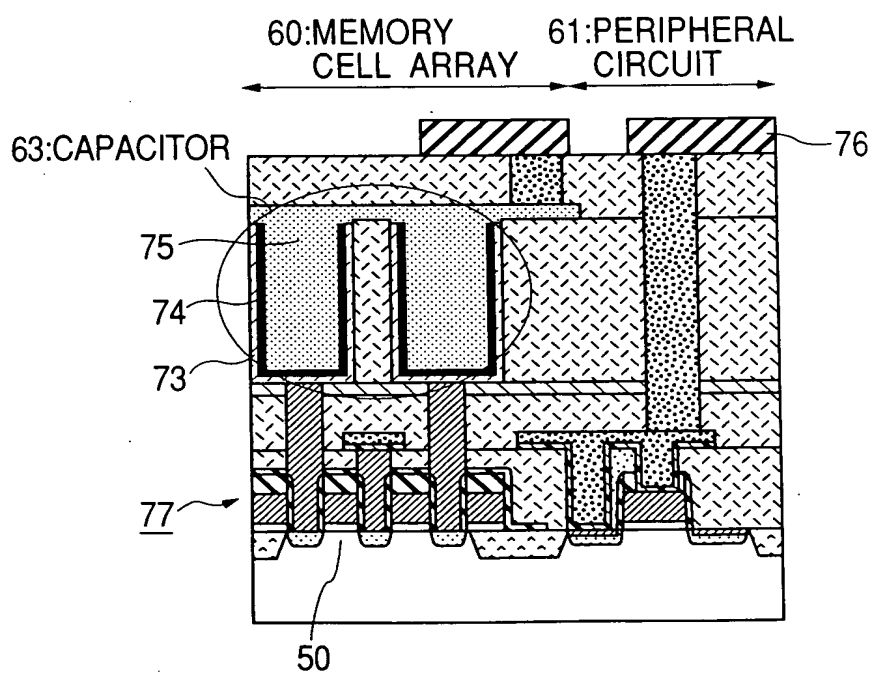
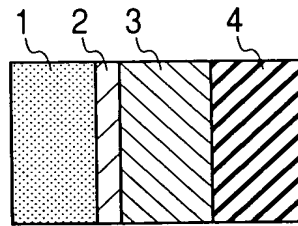
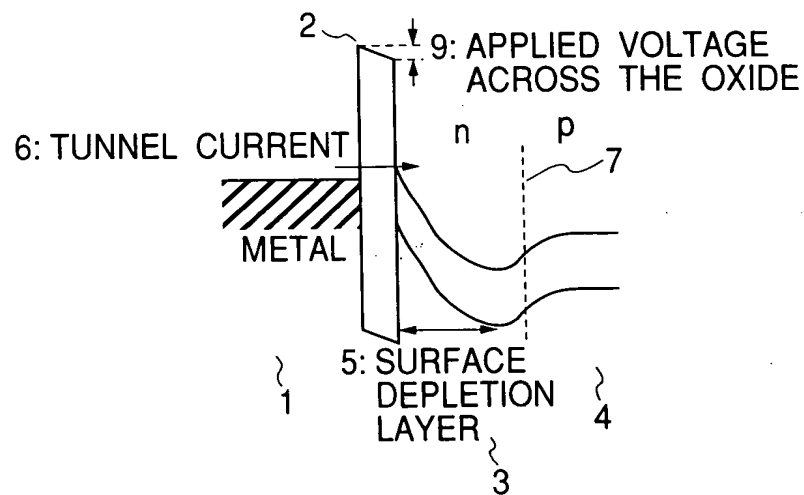


FIG. 3

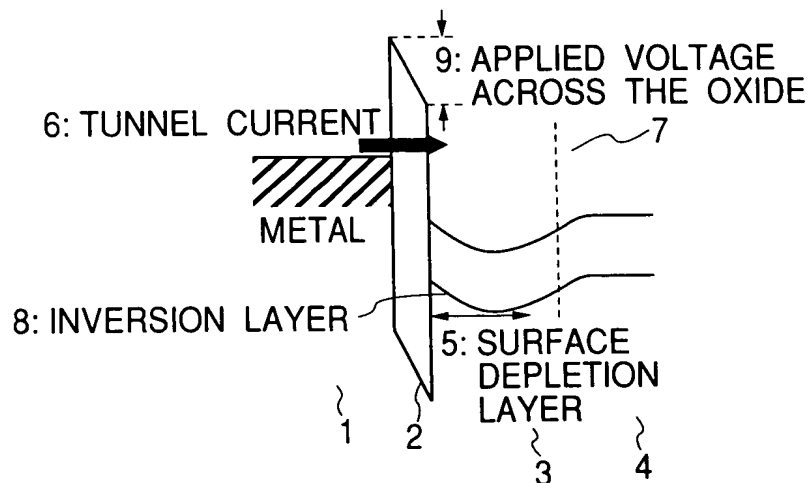
(a)



(b)



(c)



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FIG. 4

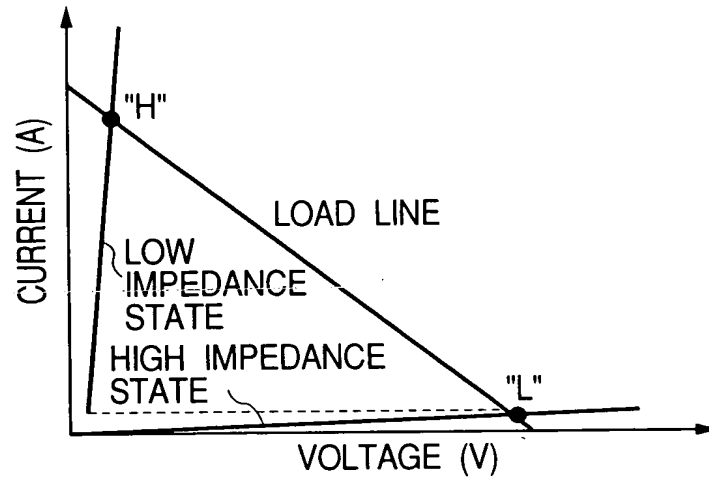
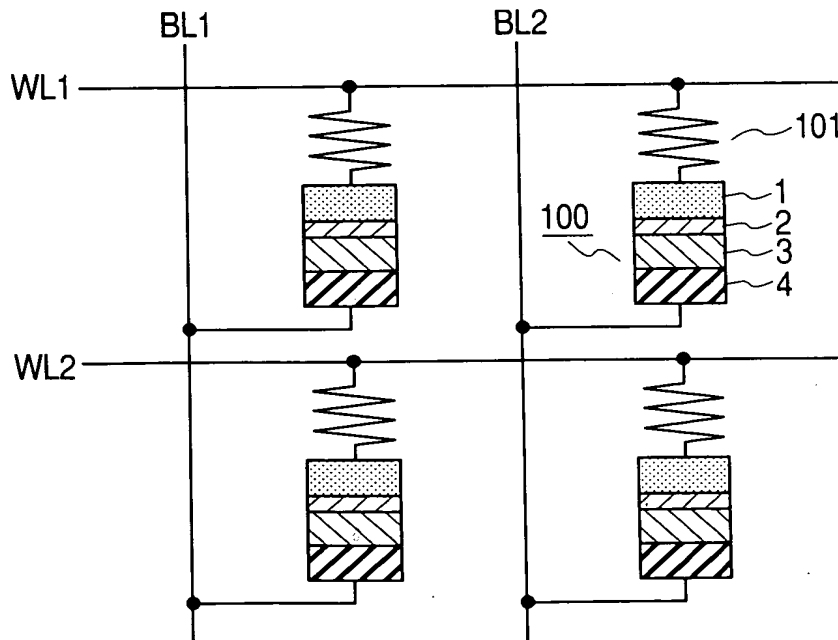


FIG. 5

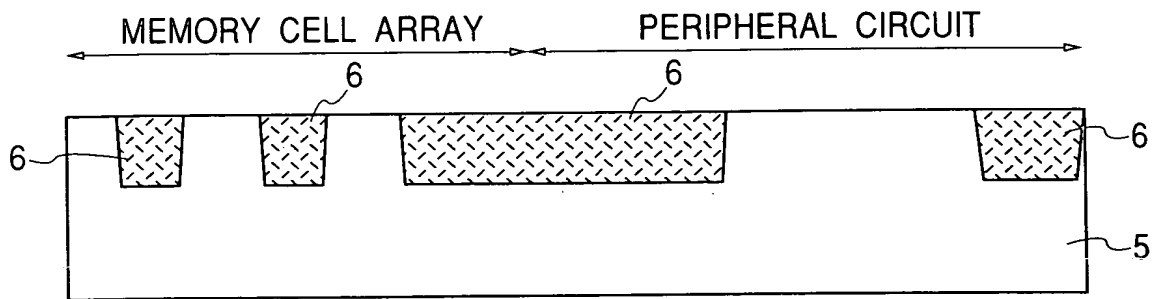


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FIG. 6

(a)



(b)

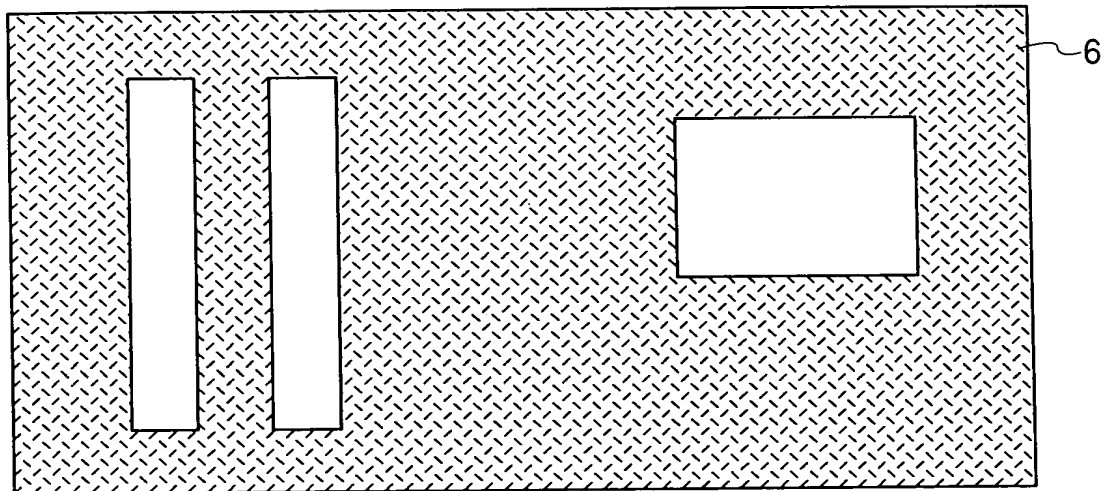


FIG. 7

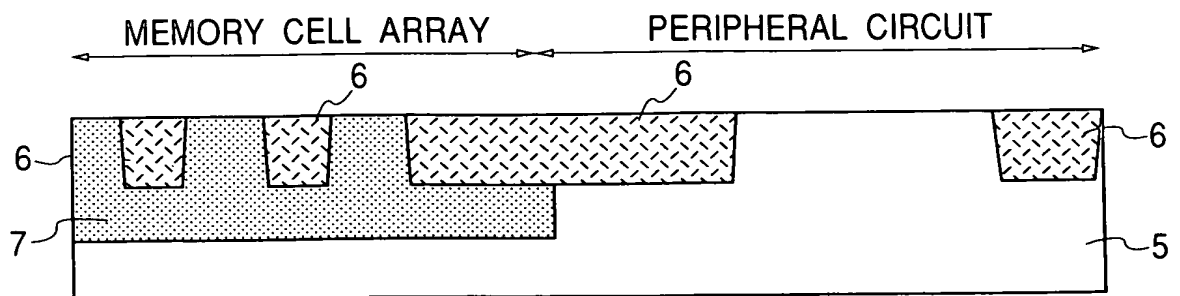


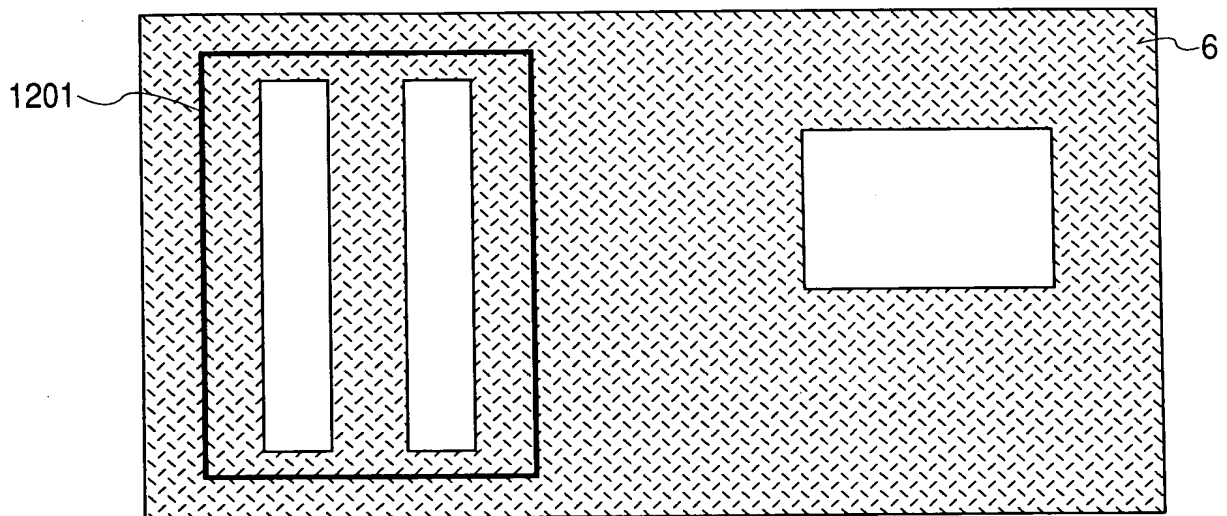
FIG. 8

FIG. 9

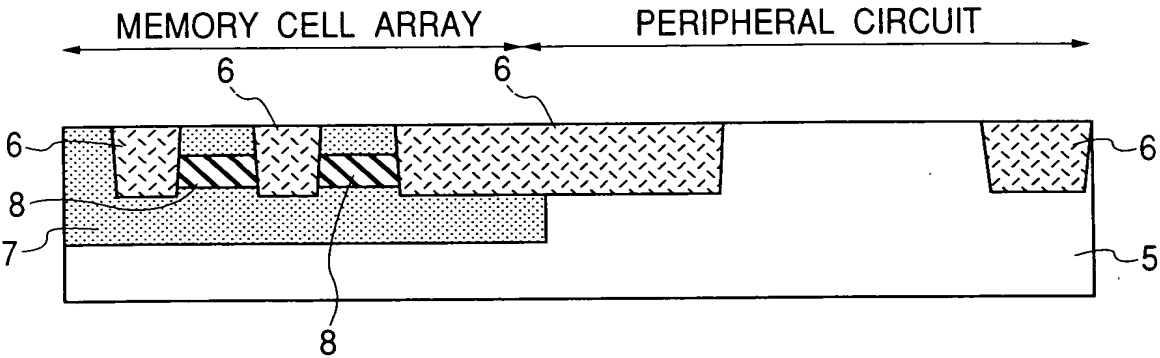
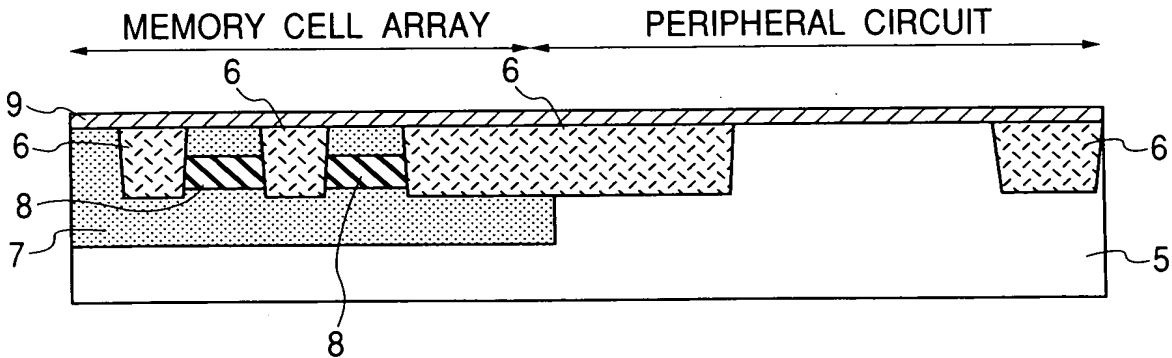
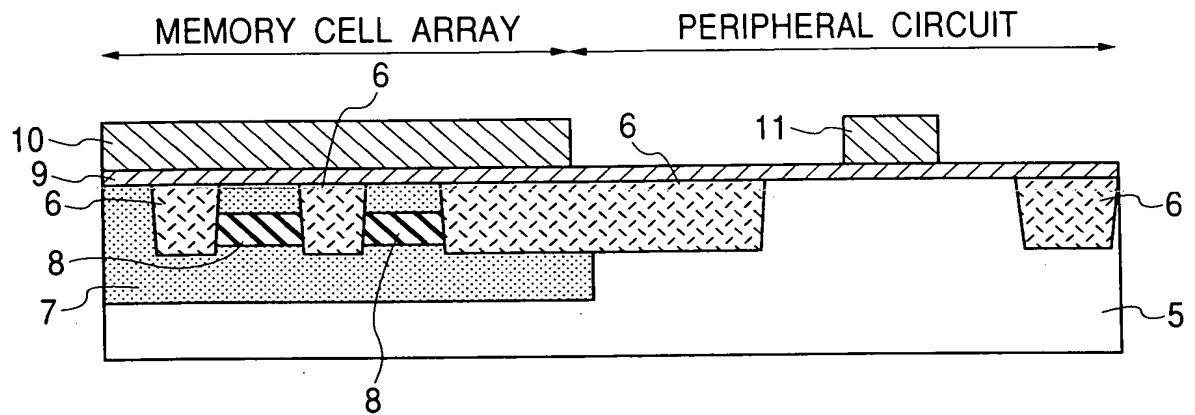
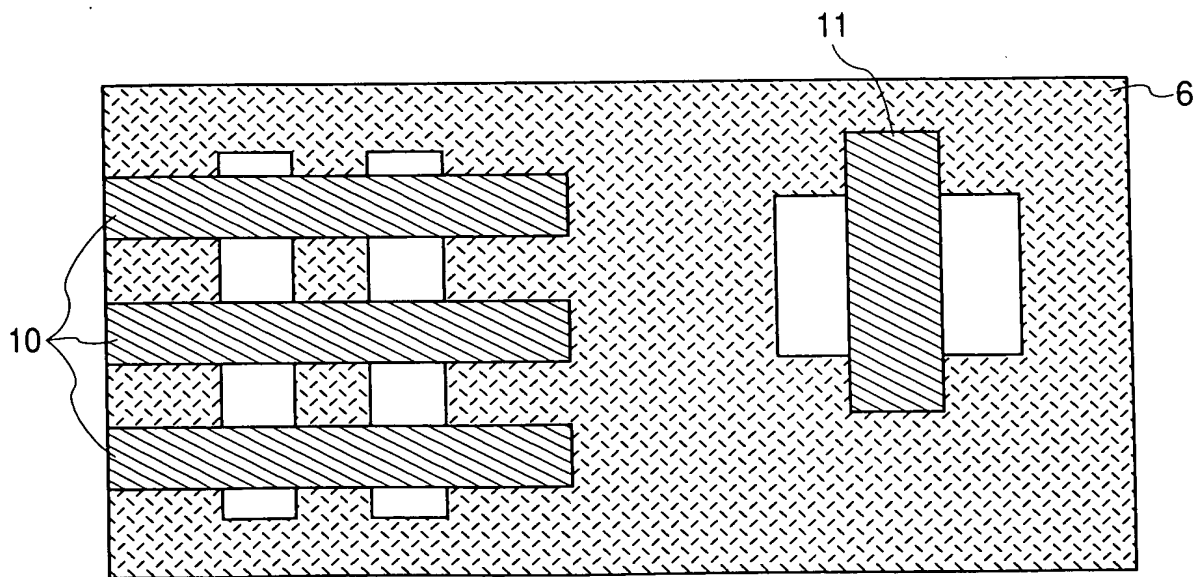
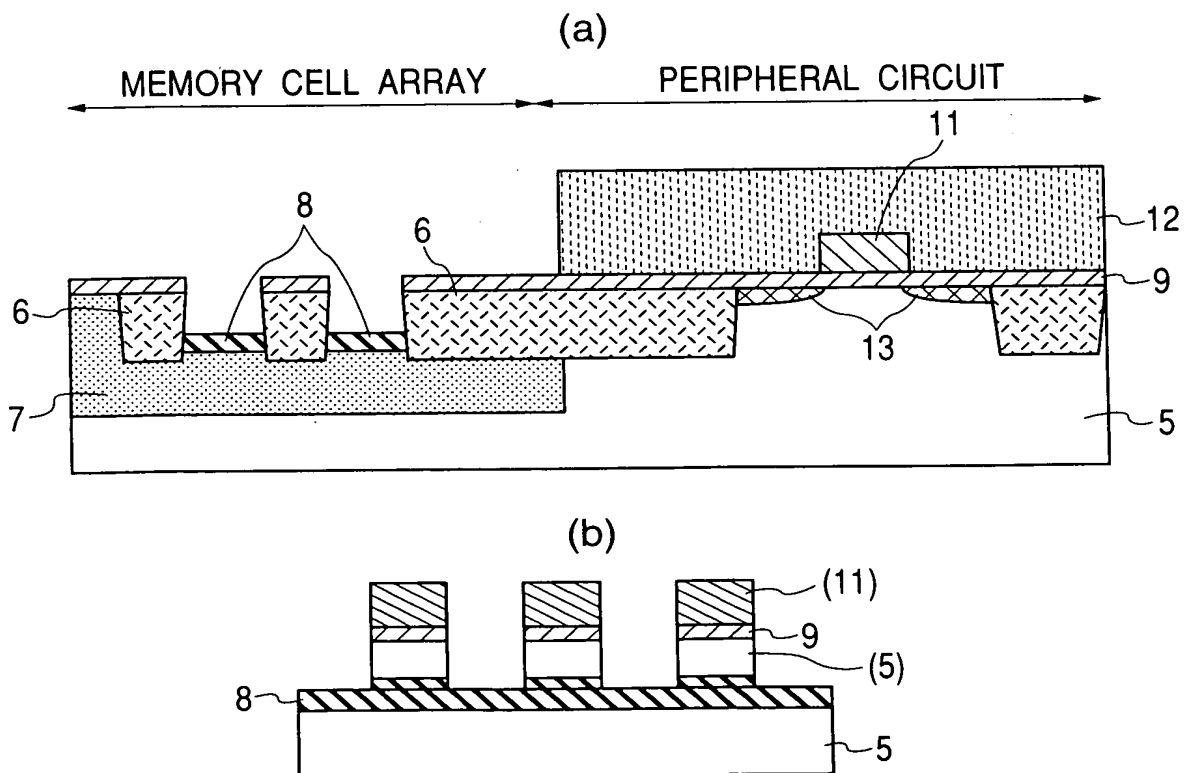


FIG. 10

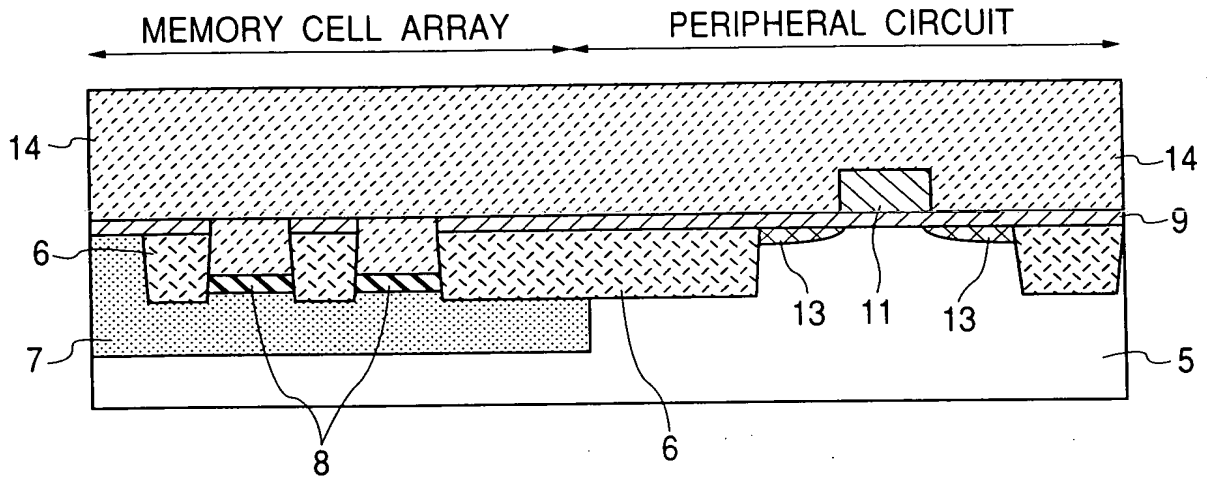
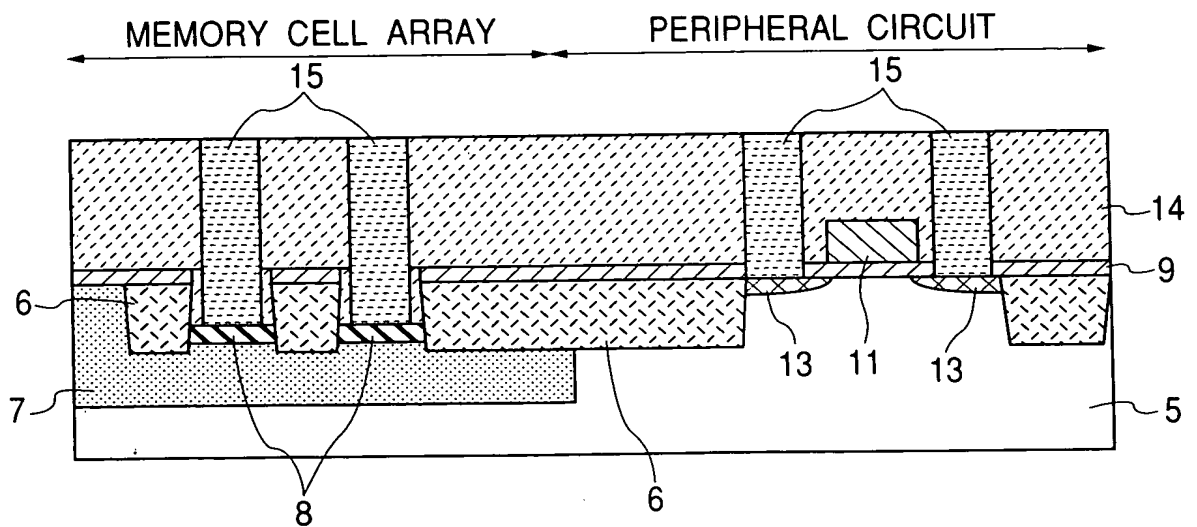


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FIG. 11**FIG. 12**



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FIG. 15**FIG. 16**

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FIG. 17

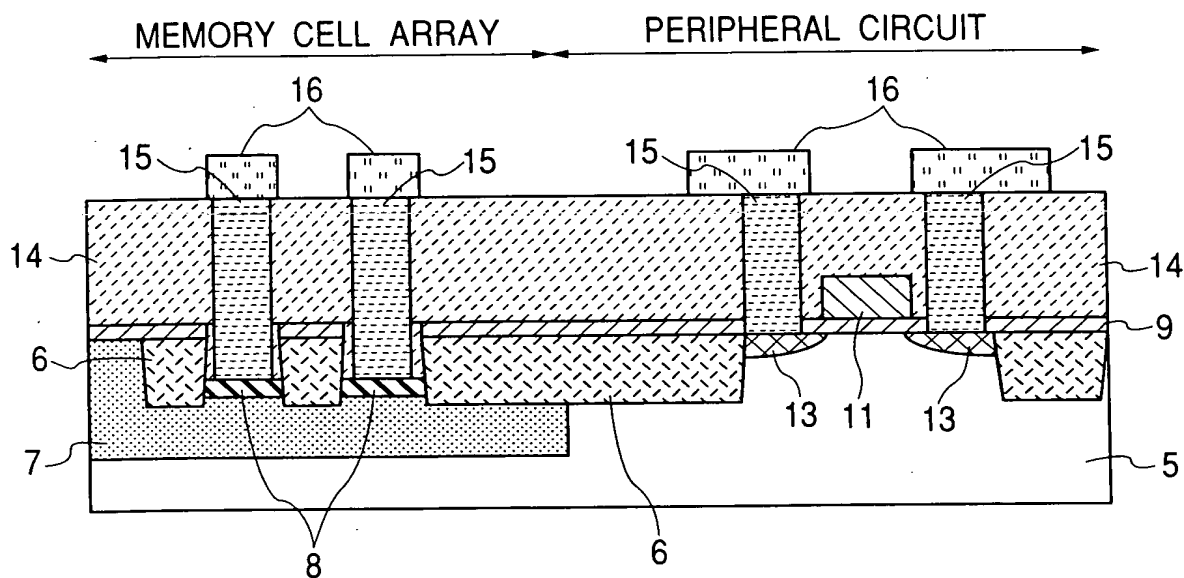
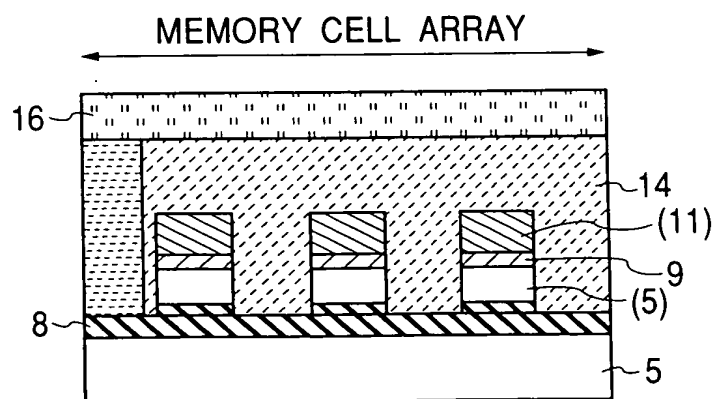


FIG. 18



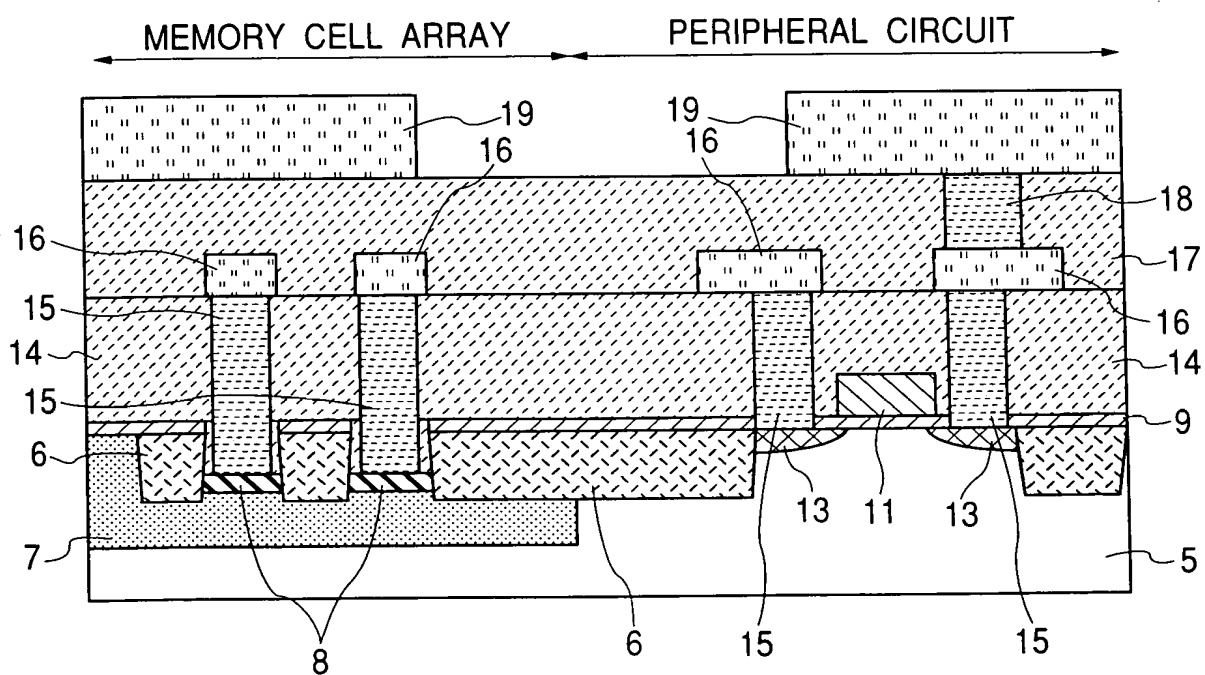


FIG. 20

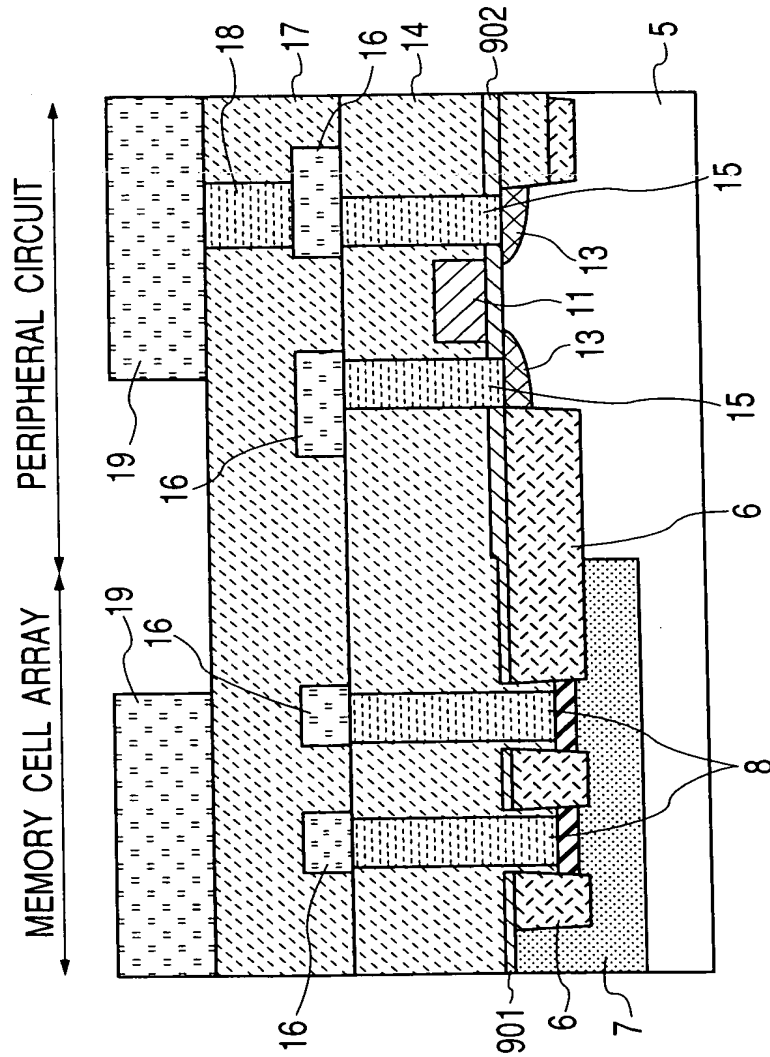


FIG. 21

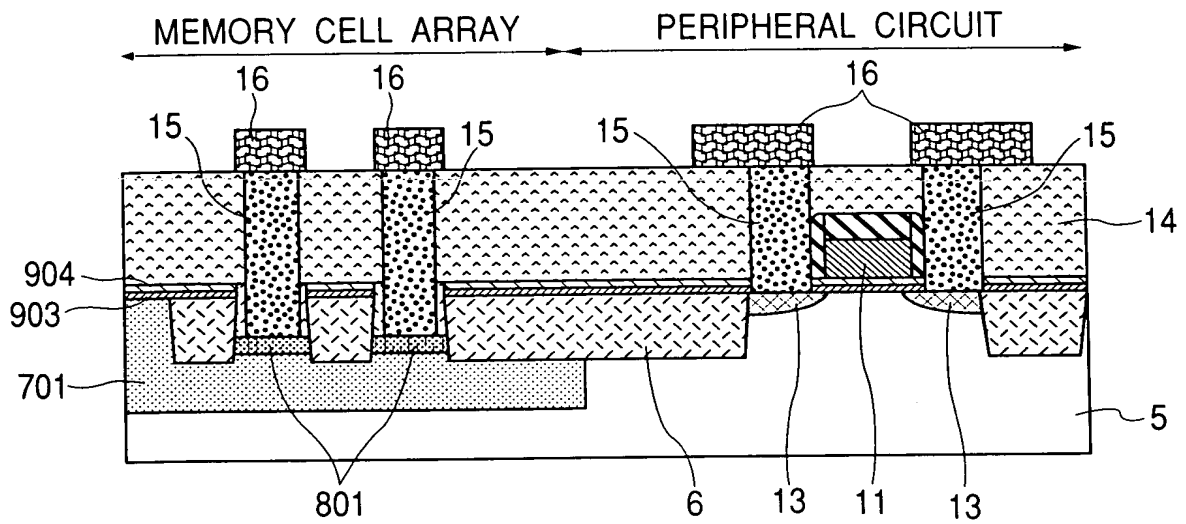


FIG. 22

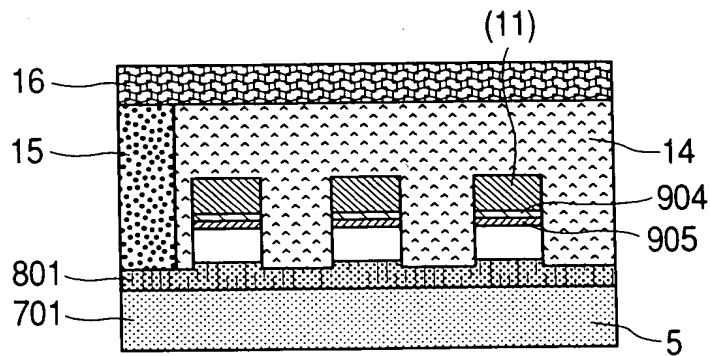
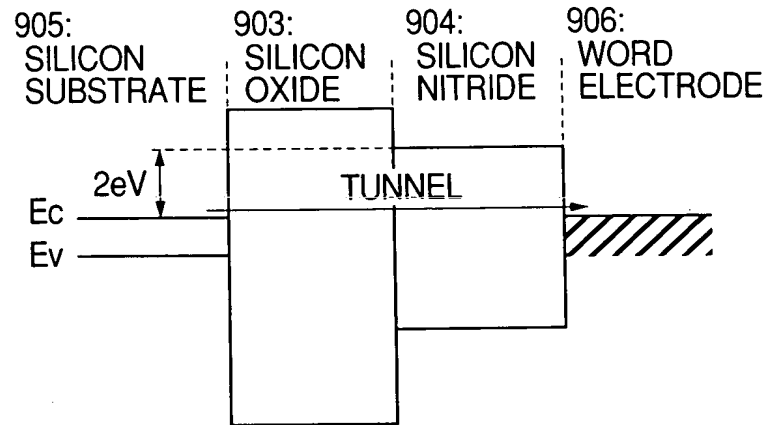
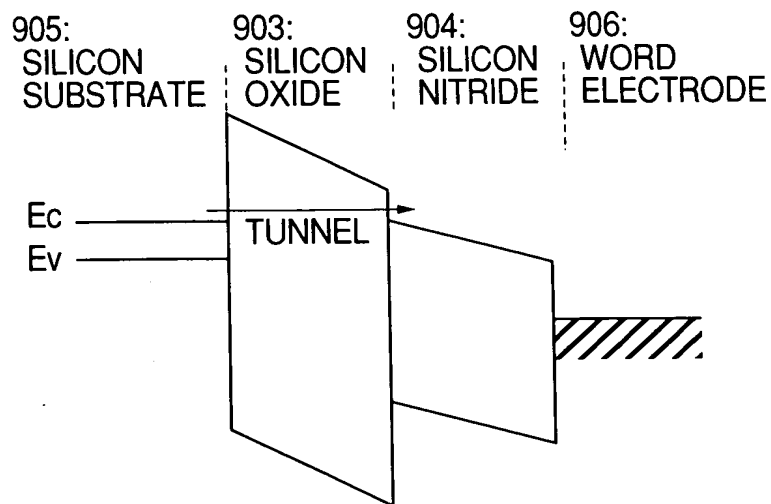


FIG. 23*FIG. 24*

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FIG. 25

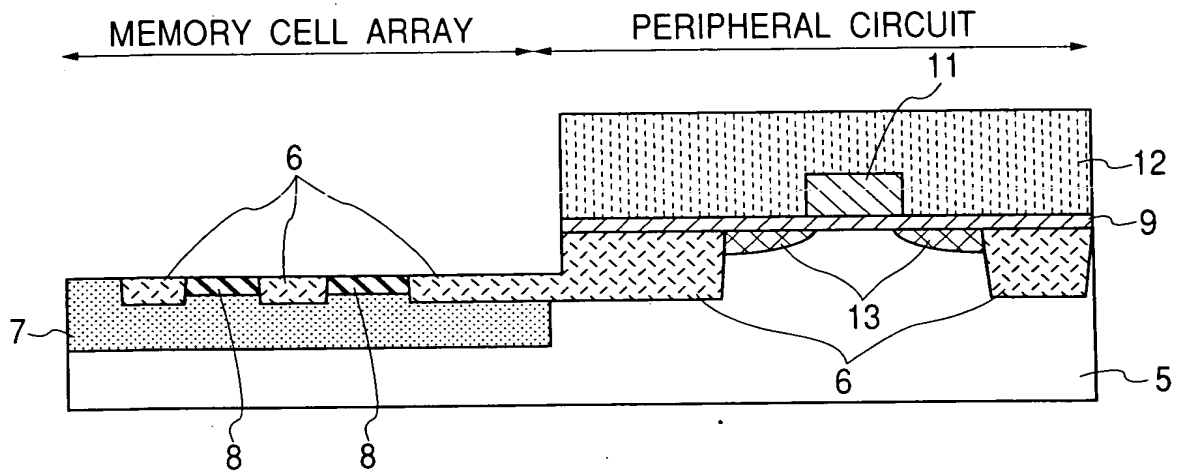


FIG. 26

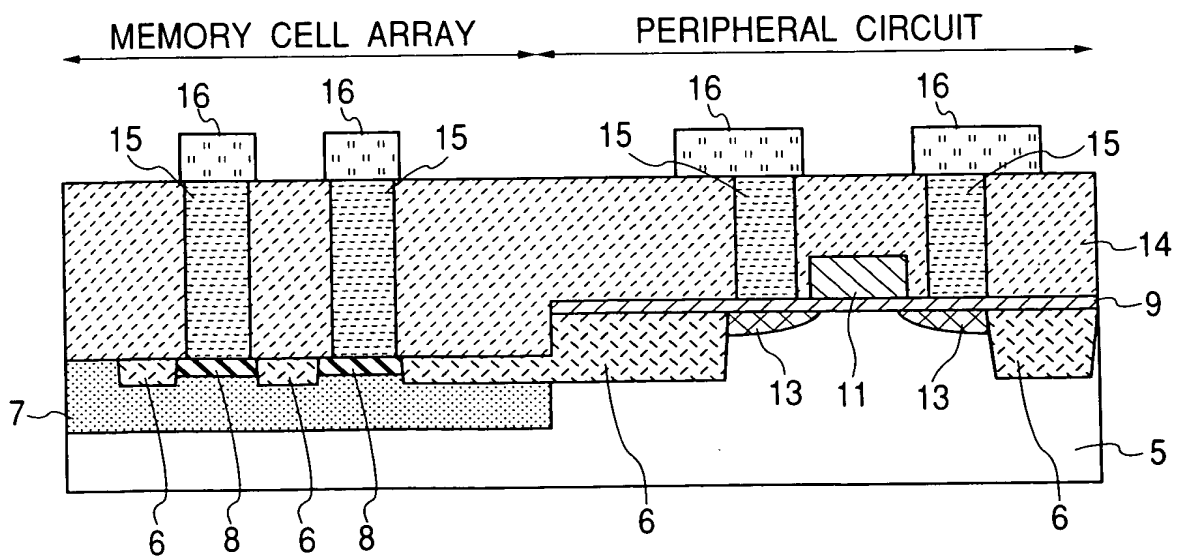


FIG. 27

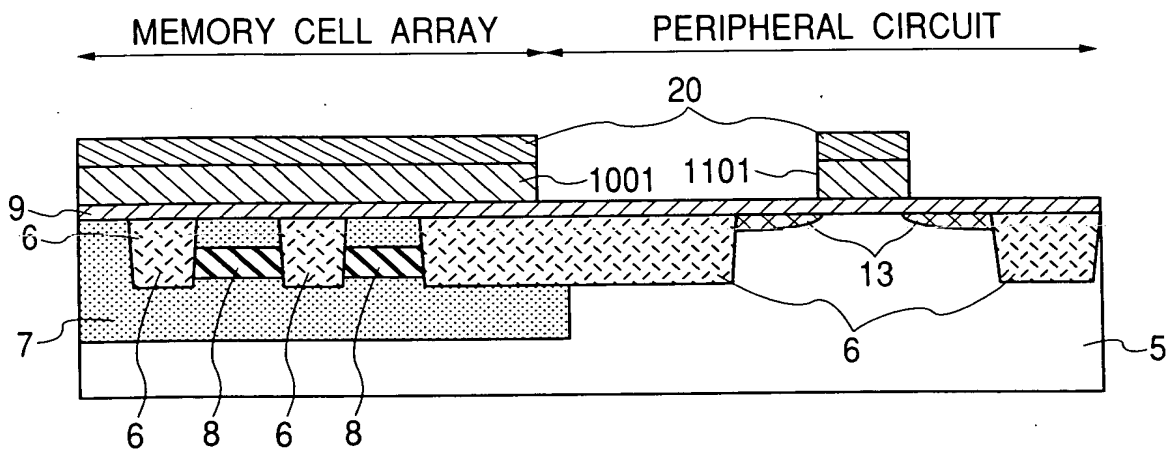


FIG. 28

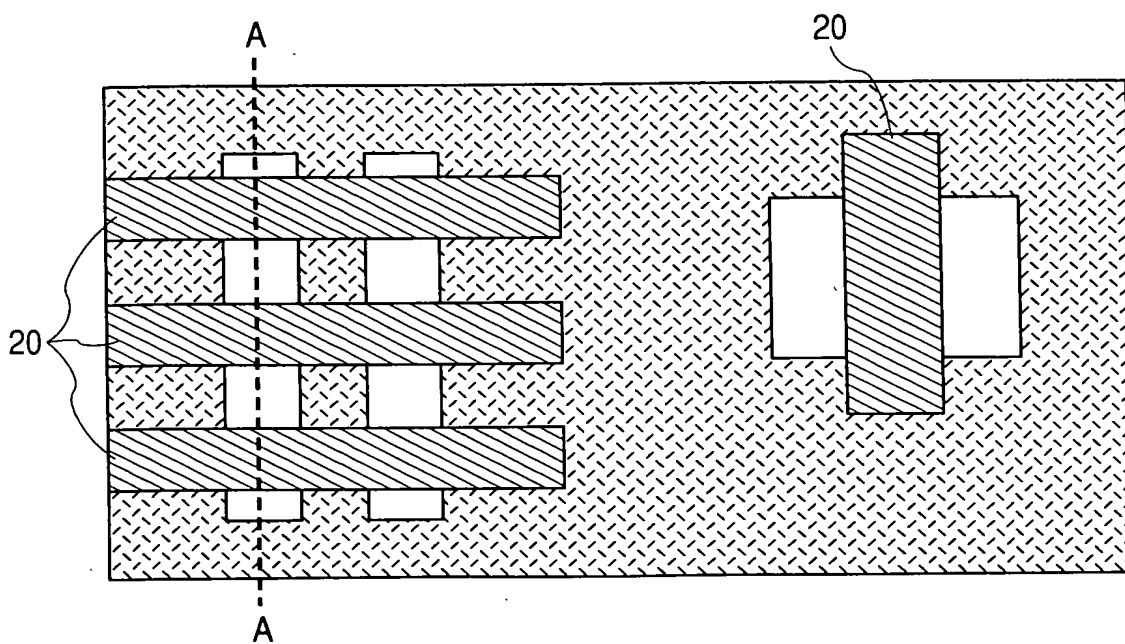
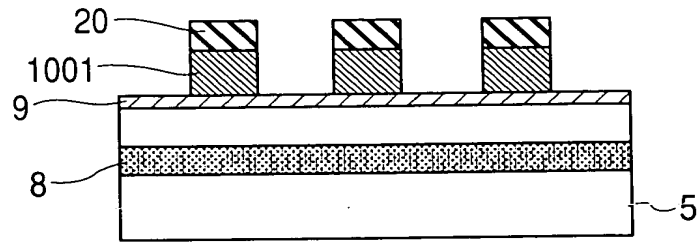
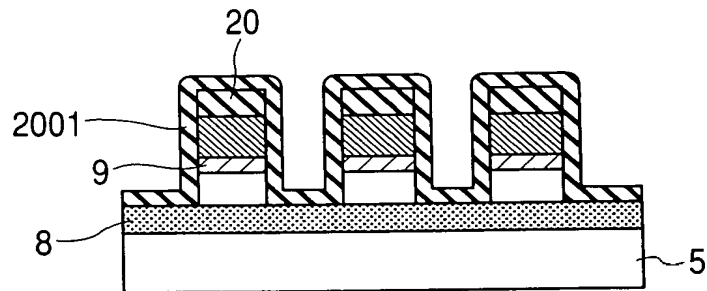
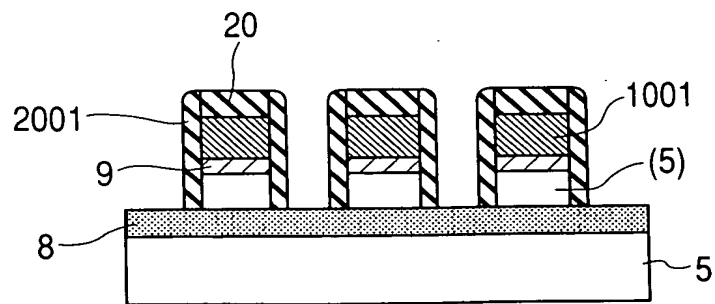


FIG. 29*FIG. 30**FIG. 31*

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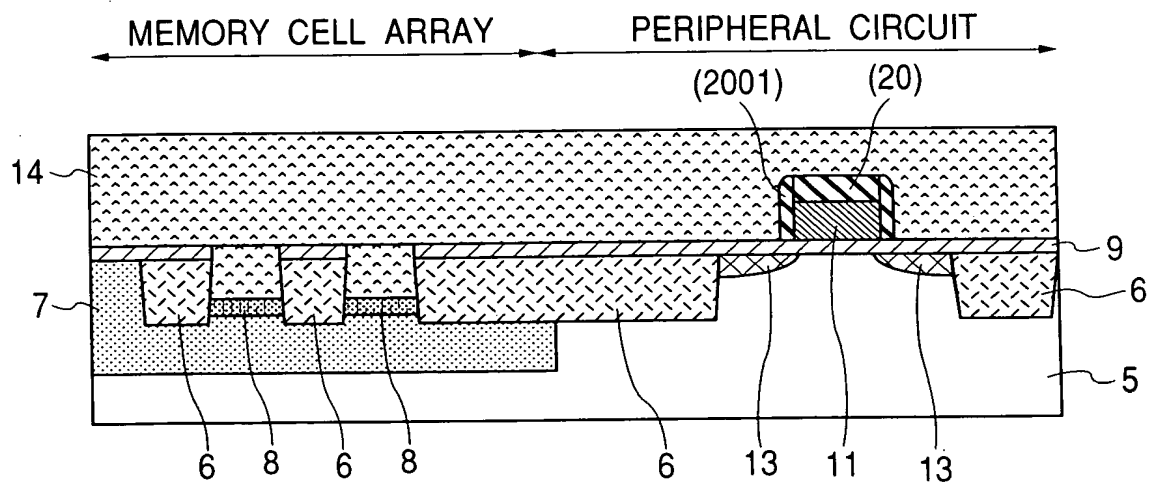
FIG. 32

FIG. 33

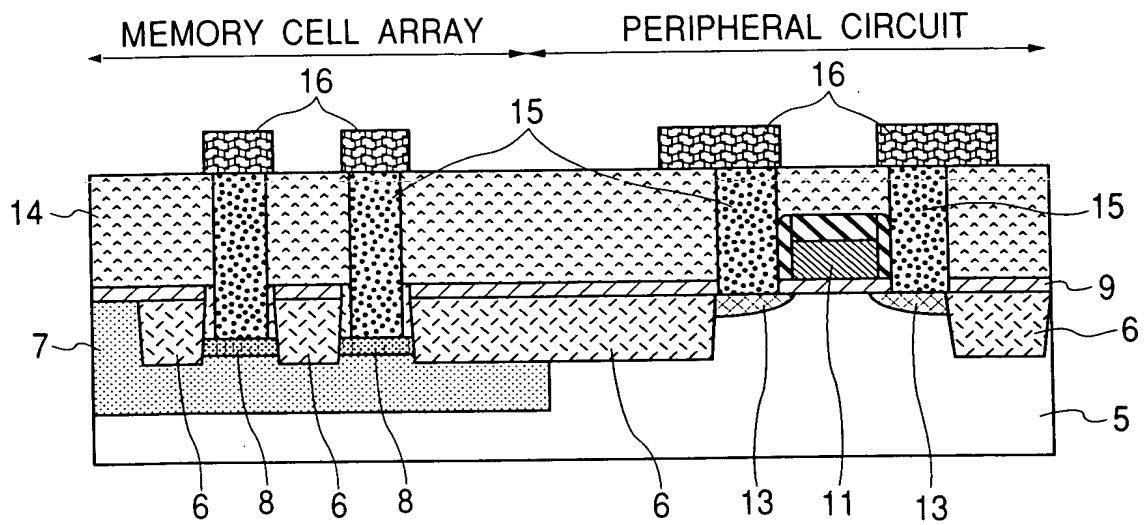
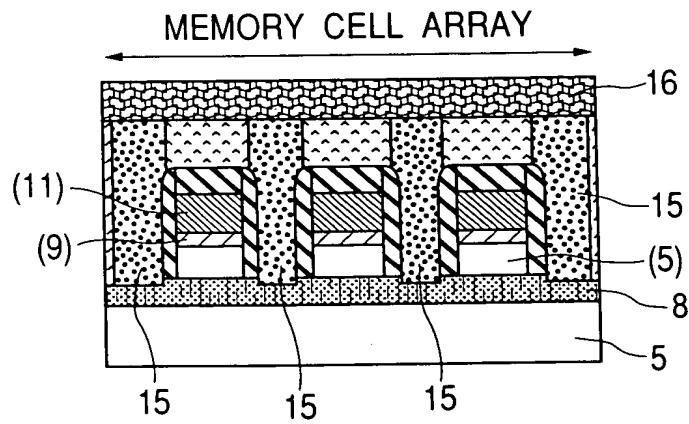
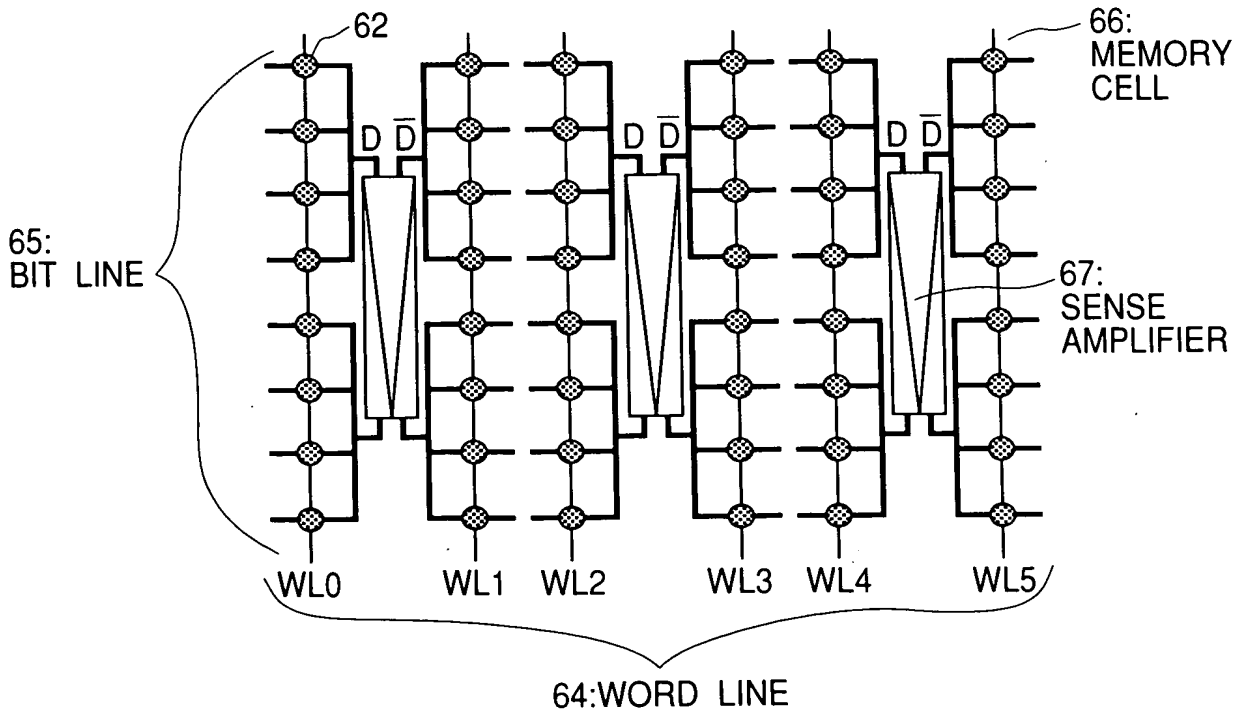


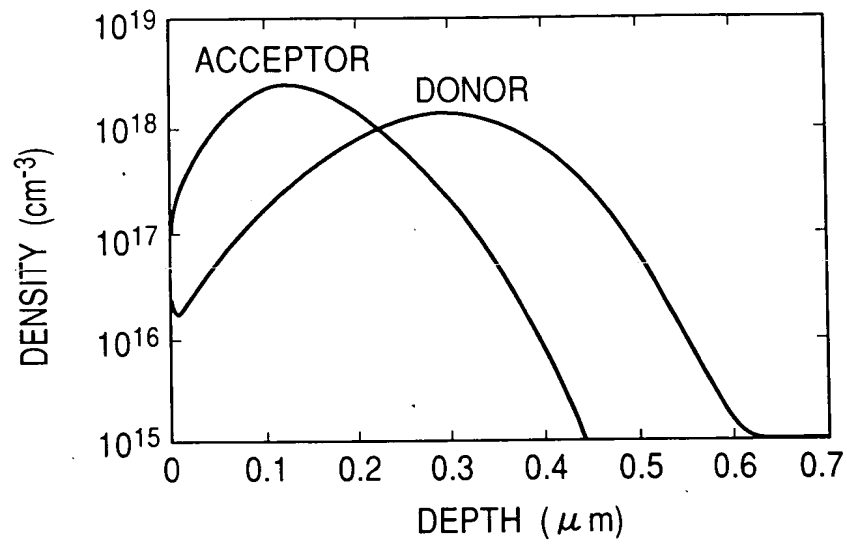
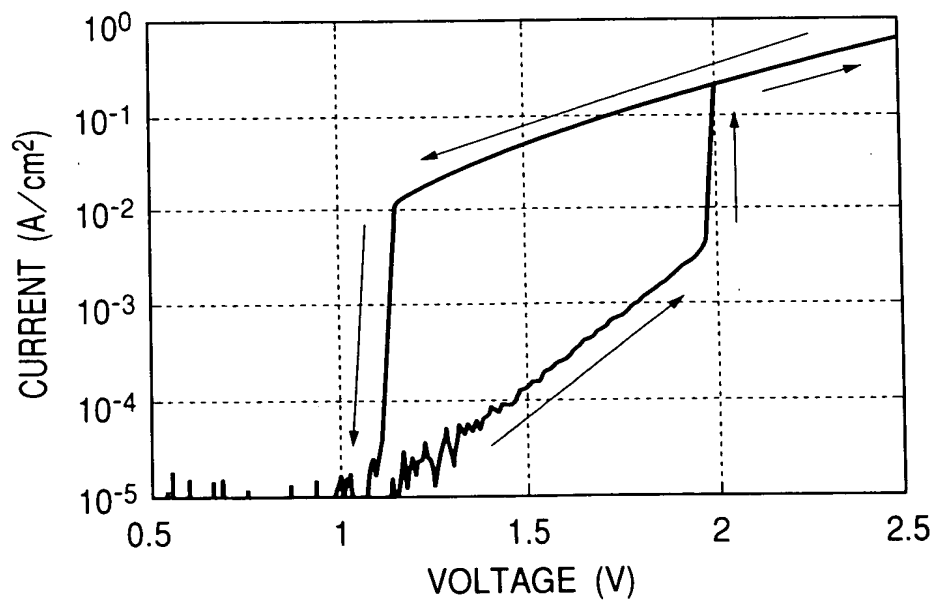
FIG. 34



A diagram showing a horizontal line with arrows at both ends. The line is divided into two sections by a vertical double-headed arrow in the center. The left section is labeled "MEMORY CELL ARRAY" and the right section is labeled "PERIPHERAL CIRCUIT".



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FIG. 37*FIG. 38*

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FIG. 41

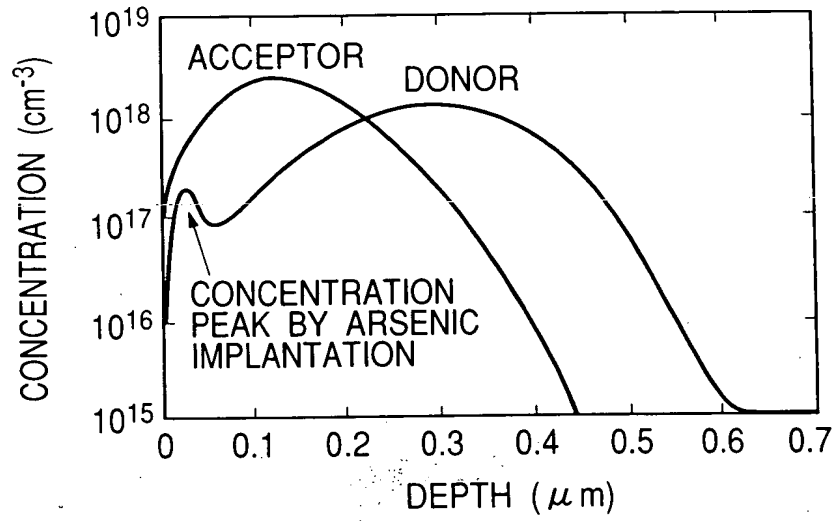
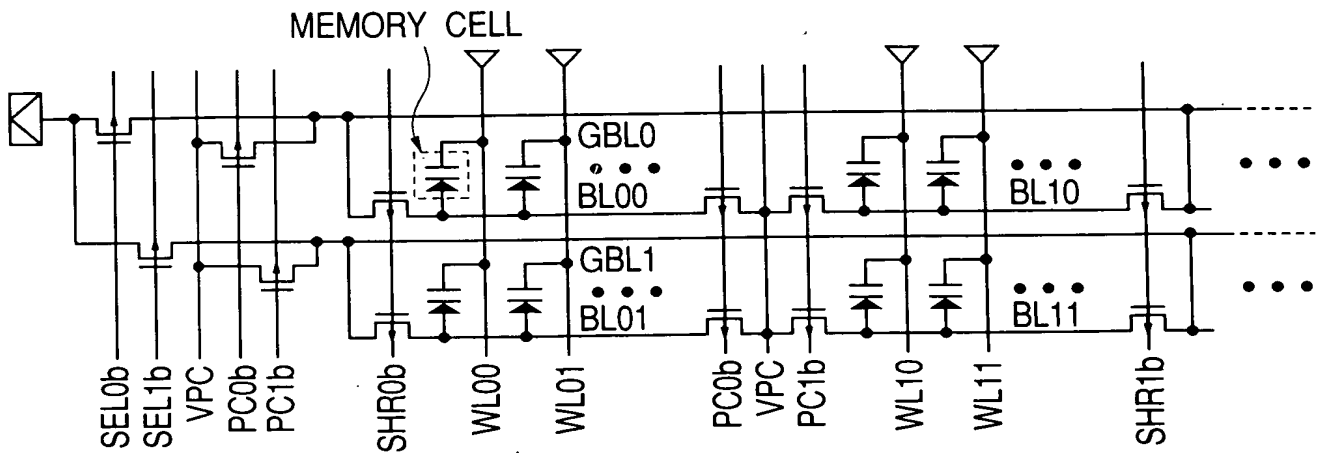


FIG. 42



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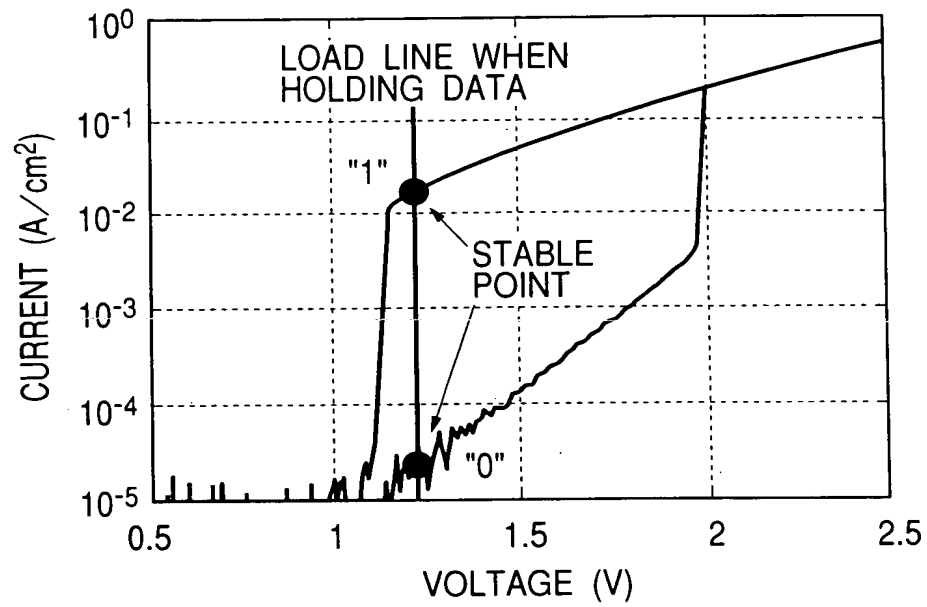
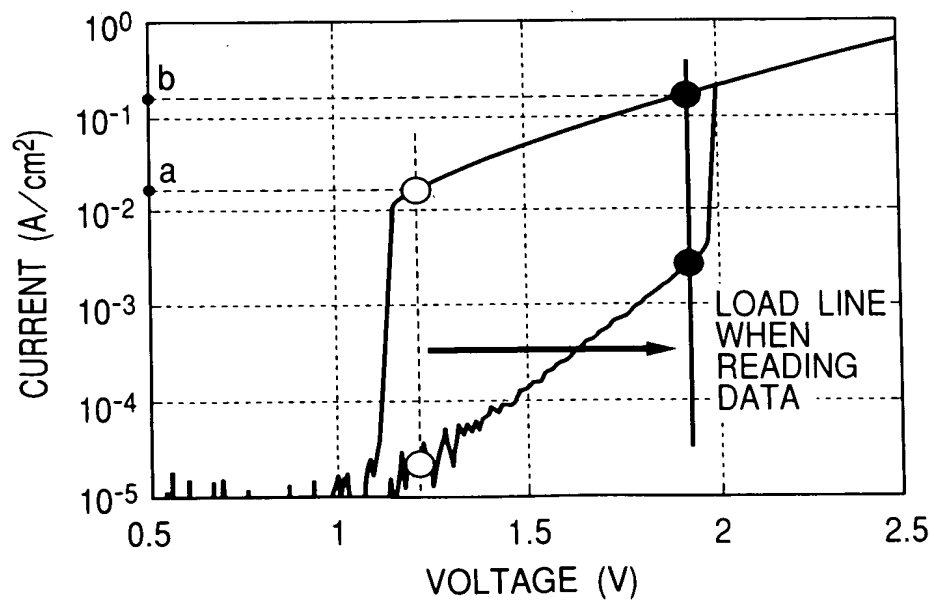
FIG. 39**FIG. 40**

FIG. 43

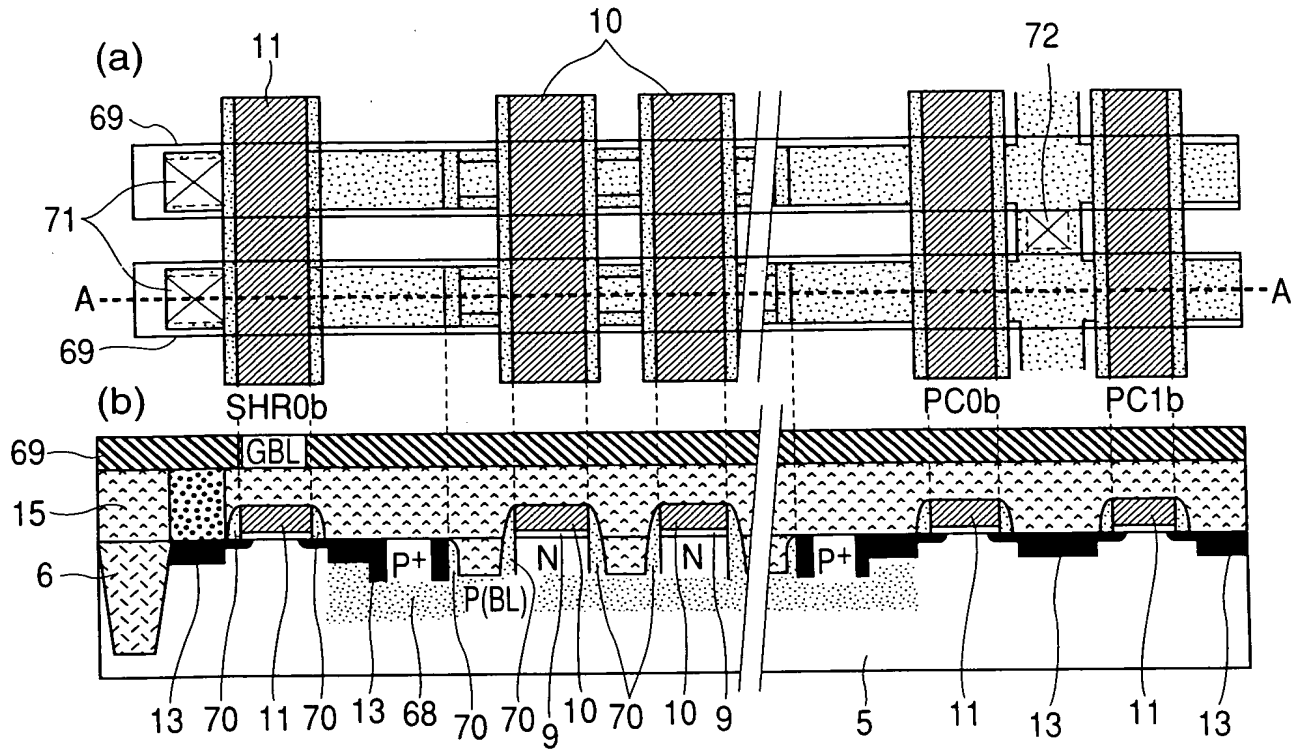
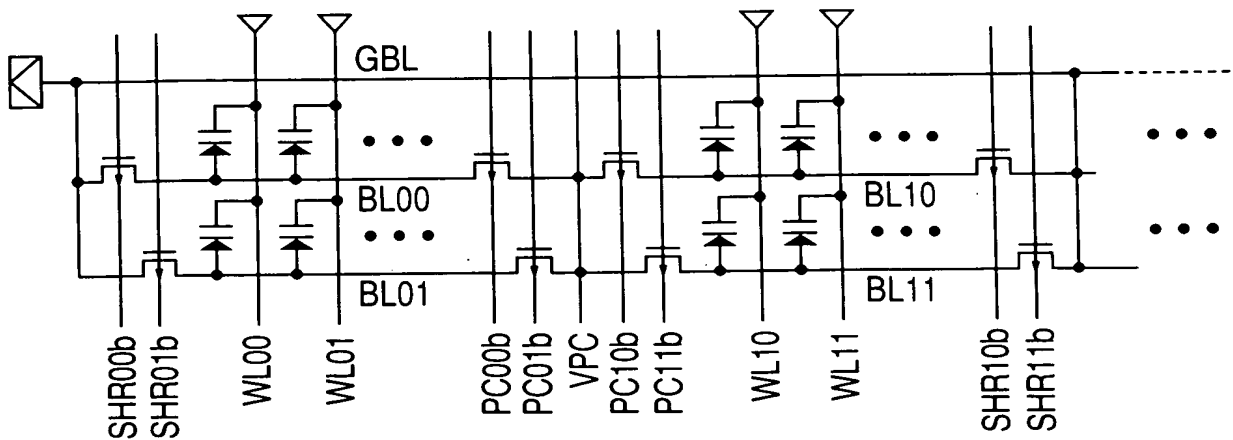


FIG. 44



100



FIG. 46

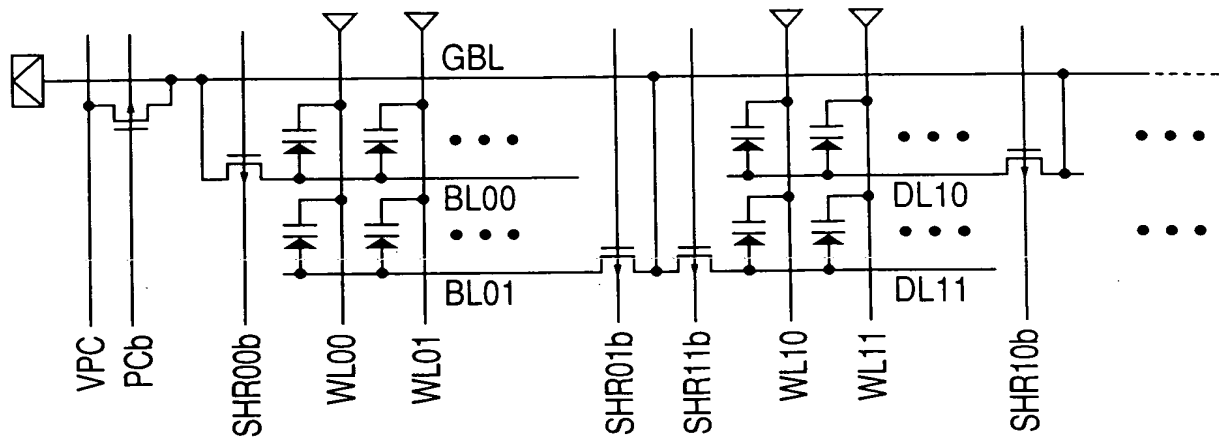


FIG. 47

